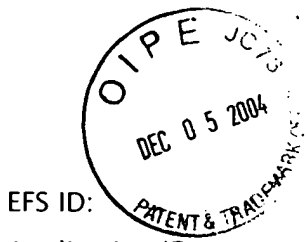


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Electronic Patent Application Submission  
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EFS ID: 73639  
Application ID: 10070091  
Title of Invention: REGISTER SET USED IN  
MULTITHREADED PARALLEL  
PROCESSOR ARCHITECTURE  
First Named Inventor: Gilbert Wolrich  
Domestic/Foreign Application: Domestic Application  
Filing Date: 2002-02-27  
Effective Receipt Date: 2004-12-05  
Submission Type: Information Disclosure Statement  
Filing Type:  
Confirmation number: 7309  
Attorney Docket Number: 10559-310US1

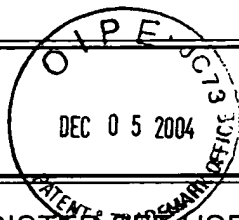


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## Title of Invention

REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR  
ARCHITECTURE

Application Number: 10/070091  
Date: 2002-02-27  
First Named Applicant: Gilbert Wolrich  
Confirmation Number: 7309  
Attorney Docket Number: 10559-310US1



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Submitted by:	Elec. Sign.	Sign. Capacity
Scott C. Harris Registered Number: 32,030	/SCH/	Attorney

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Files

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us-ids.dtd

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Comments

IDS # 1 of 4 for U.S. Application Serial No. 10/070,091

## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18  
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Title of Invention

REGISTERED SET USED IN MULTITHREADED PARALLEL  
PROCESSOR ARCHITECTURE

Application Number: 10/070091

Confirmation Number: 7309

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-310US1

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Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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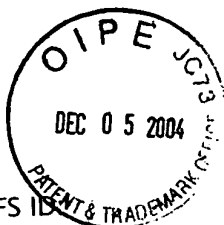
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	1	20020053017	2002-05-02	Adiletta et al.			
	2	20020056037	2002-05-09	Wolrich et al.			
	3	20030041228	2003-02-27	Rosenbluth et al.			
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EFS ID:  
Application ID:

73640

10070091



REGISTER SET USED IN  
MULTITHREADED PARALLEL  
PROCESSOR ARCHITECTURE

Title of Invention:

Gilbert Wolrich

First Named Inventor:

Domestic Application

Domestic/Foreign Application:

2002-02-27

Filing Date:

Effective Receipt Date:

2004-12-05

Submission Type:

Information Disclosure Statement

Filing Type:

Confirmation number:

7309

Attorney Docket Number:

10559-310US1

Total Fees Authorized:

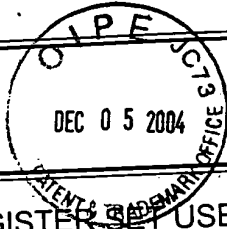
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Application Number: 10/070091  
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First Named Applicant: Gilbert Wolrich  
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Submitted by:	Elec. Sign.	Sign. Capacity
Scott C. Harris Registered Number: 32,030	/SCH/	Attorney



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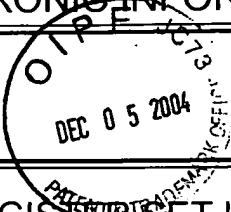
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### Comments

IDS # 2 of 4 for U.S. Application Serial No. 10/070,091

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Title of Invention

REGISTER SET USED IN MULTITHREADED PARALLEL  
 PROCESSOR ARCHITECTURE

Application Number: 10/070091

Confirmation Number: 7309

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-310US1

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## US Patent Documents

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init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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	2	5574922	1996-11-12	James			
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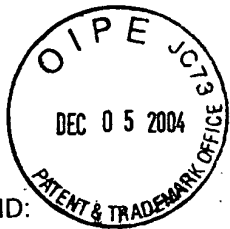
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EFS ID:  
Application ID:

73641  
10070091



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MULTITHREADED PARALLEL  
PROCESSOR ARCHITECTURE

Title of Invention:

First Named Inventor:  
Domestic/Foreign Application:  
Filing Date:  
Effective Receipt Date:  
Submission Type:

Gilbert Wolrich  
Domestic Application  
2002-02-27  
2004-12-05  
Information Disclosure Statement

Filing Type:

Confirmation number:  
Attorney Docket Number:

7309  
10559-310US1

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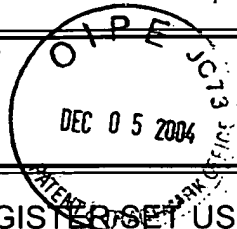
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ARCHITECTURE

Application Number: 10/070091  
Date: 2002-02-27  
First Named Applicant: Gilbert Wolrich  
Confirmation Number: 7309  
Attorney Docket Number: 10559-310US1



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Scott C. Harris Registered Number: 32,030	/SCH/	Attorney

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**Comments**

IDS # 3 of 4 for U.S. Application Serial No. 10/070,091

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## Title of Invention

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PROCESSOR ARCHITECTURE

Application Number: 10/070091

Confirmation Number: 7309

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-310US1

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## US Patent Documents

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Signature

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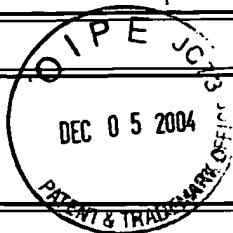
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PROCESSOR ARCHITECTURE  
First Named Inventor: Gilbert Wolrich  
Domestic/Foreign Application: Domestic Application  
Filing Date: 2002-02-27  
Effective Receipt Date: 2004-12-05  
Submission Type: Information Disclosure Statement 1  
Filing Type:  
Confirmation number: 7309  
Attorney Docket Number: 10559-310US1



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Title of Invention	REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE							
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Date:	2002-02-27							
First Named Applicant:	Gilbert Wolrich							
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Scott C. Harris Registered Number: 32,030	/SCH/	Attorney						

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**Comments**

IDS # 4 of 4 for U.S. Application Serial No. 10/070,091

## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 .  
Stylesheet Version v18.0



Title of Invention

REGISTER SET USED IN MULTITHREADED PARALLEL  
PROCESSOR ARCHITECTURE

Application Number: 10/070091

Confirmation Number: 7309

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-310US1

Search string: ( 6223238 or 6223279 or 6247025 or 6256713 or 6272616 or  
6275505 or 6279113 or 6289011 or 6298370 or 6307789 or  
6324624 or 6345334 or 6347344 or 6351808 or 6356962 or  
6373848 or 6360262 or 6389449 or 6393483 or 6415338 or  
6426940 or 6427196 or 6430626 or 6434145 or 6463072 or  
6532509 or 6552826 or 6560667 or 6577542 or 6584522 or  
6587906 or 6606704 or 6625654 or 6629237 or 6631430 or  
6631462 or 6661794 or 6667920 or 6668317 or 6681300 or  
6694380 or 4023023 or 5113516 or 5363448 or 5436626 or  
5600812 or 5652583 or 5717760 or 4992934 or 5450603 ).pn.



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Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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Signature

Examiner Name	Date

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